

05/14/01



Jc839 U.S. PTO

05-15-01

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Portland, ME 04101
270-791-1236

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09/854813



05/14/01

Box Patent Application
Assistant Commissioner of Patents
Washington, D.C. 20231

Transmitted herewith for filing is the patent application of:

Applicants Johnson, Eric H.; Harley-Stead, Michael W.
Title: A System to Enable Photolithography on Severe Structure Topologies
Attorney Docket No. 17732-3675000

Pages of Specification:	11	Pages of Claims:	4 (w/ 24 total/3 indep.)
Pages of Abstract:	1	Sheets of Drawings:	3
Signed Declaration:	2	Assignee:	Fairchild Semiconductor Corporation
Assignment with Recordation Cover:	3		

Basic filing fee	710.00
Total claims in excess of 20 x 18.00	72.00
Independent claims in excess of 3 x \$80.00	- 0 -
Multiple dependent claims	- 0 -
Recordation fee (if applicable)	40.00
TOTAL FILING FEE	822.00

☒ A check in the amount of \$822.00 is enclosed.

Please charge Deposit Account No. 500282 for any deficiency of fees as follows:

- ☒ Filing Fee
☒ Any additional fees associated with this paper or during the pendency of this application.

Respectfully submitted,

5/14/01
Date

Patrick R. Scanlon
Patrick R. Scanlon, Reg. No. 34,500
Attorney for Applicants



27723

PATENT TRADEMARK OFFICE

CERTIFICATE OF MAILING

"EXPRESS MAIL Mailing Label Number: EL469488513US Date of Deposit: May 14, 2001

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Constance S. Rogers
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11002 U.S. 270
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